

	1	2	3	4	5	6																																								
A						The Stackup Legend below this is static. If you change the stackup, update the Legend.																																								
B						Layer Stack Up Detail for: BoosterPack_40pin_Template_EVM.PcbDoc <table><tr><th>Layer Name</th><th>Gerber Document</th><th>Copper Thickness</th><th>Dielectric Material</th></tr><tr><td>Top Solder Mask</td><td>(.GTS)</td><td></td><td>Solder Resist</td></tr><tr><td>Top Layer</td><td>(.GTL)</td><td>1.4mil</td><td>FR-4 High Tg</td></tr><tr><td>MidLayer1 (GND)</td><td>(.GI)</td><td>1.4mil</td><td>FR-4 High Tg</td></tr><tr><td>MidLayer2 (PWR)</td><td>(.G2)</td><td>1.4mil</td><td>FR-4 High Tg</td></tr><tr><td>Bottom Layer</td><td>(.GBL)</td><td>1.4mil</td><td>FR-4 High Tg</td></tr><tr><td>Bottom Solder Mask</td><td>(.GBS)</td><td></td><td>Solder Resist</td></tr></table>	Layer Name	Gerber Document	Copper Thickness	Dielectric Material	Top Solder Mask	(.GTS)		Solder Resist	Top Layer	(.GTL)	1.4mil	FR-4 High Tg	MidLayer1 (GND)	(.GI)	1.4mil	FR-4 High Tg	MidLayer2 (PWR)	(.G2)	1.4mil	FR-4 High Tg	Bottom Layer	(.GBL)	1.4mil	FR-4 High Tg	Bottom Solder Mask	(.GBS)		Solder Resist												
Layer Name	Gerber Document	Copper Thickness	Dielectric Material																																											
Top Solder Mask	(.GTS)		Solder Resist																																											
Top Layer	(.GTL)	1.4mil	FR-4 High Tg																																											
MidLayer1 (GND)	(.GI)	1.4mil	FR-4 High Tg																																											
MidLayer2 (PWR)	(.G2)	1.4mil	FR-4 High Tg																																											
Bottom Layer	(.GBL)	1.4mil	FR-4 High Tg																																											
Bottom Solder Mask	(.GBS)		Solder Resist																																											
C	<div><div><div>2300.00mil</div><div>2550.00mil</div><div>Board Edge</div></div><div><div>Drill Table</div><div>DRILL TOLERANCE FOR PTH +/- 3MIL</div><div>7.874mil drill:+0/-7.874mil</div><div>12mil drill:+0/-12mil</div><div>16mil drill:+0/-16mil</div></div></div> <table><tr><th>Symbol</th><th>Hit Count</th><th>Tool Size</th><th>Plated</th><th>Hole Type</th></tr><tr><td>○</td><td>17</td><td>7.874mil (0.2mm)</td><td>PTH</td><td>Round</td></tr><tr><td>*</td><td>13</td><td>12mil (0.305mm)</td><td>PTH</td><td>Round</td></tr><tr><td>▽</td><td>96</td><td>16mil (0.406mm)</td><td>PTH</td><td>Round</td></tr><tr><td>×</td><td>28</td><td>40mil (1.016mm)</td><td>PTH</td><td>Round</td></tr><tr><td>⊗</td><td>6</td><td>40.157mil (1.02mm)</td><td>PTH</td><td>Round</td></tr><tr><td>⊙</td><td>40</td><td>45.276mil (1.15mm)</td><td>PTH</td><td>Round</td></tr><tr><td></td><td>200 Total</td><td></td><td></td><td></td></tr></table> <div>Drill Table DRILL TOLERANCE FOR PTH +/- 3MIL 7.874mil drill:+0/-7.874mil 12mil drill:+0/-12mil 16mil drill:+0/-16mil</div>					Symbol	Hit Count	Tool Size	Plated	Hole Type	○	17	7.874mil (0.2mm)	PTH	Round	*	13	12mil (0.305mm)	PTH	Round	▽	96	16mil (0.406mm)	PTH	Round	×	28	40mil (1.016mm)	PTH	Round	⊗	6	40.157mil (1.02mm)	PTH	Round	⊙	40	45.276mil (1.15mm)	PTH	Round		200 Total				
Symbol	Hit Count	Tool Size	Plated	Hole Type																																										
○	17	7.874mil (0.2mm)	PTH	Round																																										
*	13	12mil (0.305mm)	PTH	Round																																										
▽	96	16mil (0.406mm)	PTH	Round																																										
×	28	40mil (1.016mm)	PTH	Round																																										
⊗	6	40.157mil (1.02mm)	PTH	Round																																										
⊙	40	45.276mil (1.15mm)	PTH	Round																																										
	200 Total																																													
D	<div><div>1000.00mil</div></div>					<div>DESIGN INFORMATION</div> <div>BOARD SIZE (REFER ALSO ARRAY/PANEL PROFILING INFORMATION) 2300.00MIL X 2550.00MIL Number of Layers : 4 MIN. TRACK WIDTH: 8 MIL MIN. CLEARANCE: 7.865MIL MIN. VIA PAD SIZE: 19.685MIL MINIMUM ANNULAR RING 0.149mm (5.905MIL)EXTERNAL PER IPC-D-275 CLASS 2 LEVEL C REGISTRATION TOLERANCES: METAL +/- 5 MIL, HOLES +/- 3 MIL</div> <div>MATERIAL: <div><div>FR-408</div><div>X FR-4 High Tg</div><div>OTHER</div></div><div>THICKNESS: X 63 MIL (1.6mm) +/-10% OTHER</div><div>TOLERANCE: X ANSI IPC-6012 TYPE 3 CLASS 2 OTHER +/-</div><div>BOW & TWIST: X ANSI IPC-6012 TYPE 3 CLASS 2 OTHER +/-</div><div>COPPER THICKNESS (FINISHED): OUTER: X 1.4MIL (1oz) 2MIL (1.4oz) 2.8MIL (2oz) INNER SIGNAL: X 1.4MIL (1oz) 2.8MIL (2oz) N/A</div><div>DRILLING: REFERENCE: X AS SHOWN X NC_DRILL FILES</div><div>PTH MIN COPPER THICKNESS: X 1MIL OTHER</div><div>BOARD FINISH: SILKSCREEN: X TOP X BOTTOM SILKSCREEN COLOR: X WHITE OTHER</div><div>SOLDER RESIST COLOR: X GREEN BLUE OTHER</div><div>SURFACE FINISH: X IMMERSION GOLD (ENIG) ENEPIG IMM. TIN/SILVER OR EQUIV OTHER</div><div>ARRAY/PANEL: CUT AND TRIM PER MECH LAYER 1 N.C. ROUTE X V. SCORE</div><div>CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBs TO MEET OR EXCEED THE REQUIREMENTS OF: X ANSI IPC-A-600F CLASS -> 1 X 2 3 X UL 94V-0 X RoHS OTHER PER ORDER</div><div>ADDITIONAL REQUIREMENTS: VIA TENTING X YES NO MICROSECTION: YES IMPEDANCE CONTROL: YES NO X BARE BOARD ELEC. TEST: NONE X REQUIRED PER ORDER MANUFACTURER'S UL: RAIL METAL X SILK</div></div> <div><div>TEXAS INSTRUMENTS</div><div>PROJECT TITLE: Inductive Proximity Switch BoosterPack</div><div>DESIGNED FOR: Public Release</div><div>FILE NAME: TIDA-00563_EI.PcbDoc</div><div>ENGINEER: FABC</div><div>LAYOUT BY: SY</div><div>ALTUM DESIGNER VERSION: 14.3.18.45973</div><div>SCALE: 1.00</div></div>																																								
	ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-00563	REV: E1	SUN REV: Not In VersionControl	Texas Instruments (TI) and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. TI and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. TI and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.																																									
	LAYER NAME= Drill Drawing																																													
	PLOT NAME=Fabrication Drawing	GENERATED : 10/19/2015 1:30:22 PM	TEXAS INSTRUMENTS																																											
	1	2	3	4	5																																									
					6																																									